

In The Specification:

Please amend the paragraph beginning at page 15, line 1, as follows:

The clamping control unit ~~+20-~~130 is a PMOS transistor. The first and second ends of the PMOS transistor are connected to the output terminal NOUT and the ground voltage VSS, respectively, and the control voltage V1 is applied to the gate thereof.

Please amend the paragraph beginning at page 15, line 20, as follows:

As described above, the clamping control unit ~~+20-~~130 is used to maintain the reference voltage VREF at a constant level.

Please amend the paragraph beginning at page 25, line 20, as follows:

A first terminal of the first control transistor CTR1 is connected between the first resistor R1 and the second resistor R2, and the first operating mode signal MODE1 is applied to the gate thereof. A second terminal of the first control transistor CTR1 is connected to a first node N1 between the second resistor R2 and the third resistor R3.